Design thought process 48V module.

1. Architecture

The chip LTC6811 can only monitor 12S battery so we are limited by this feature. At the same time we want to have a current of about 100A in peak, we need to use high discharge current cells. If we use 20A cells we need to do at least 5P battery.

If we do a 12s5p battery it will be fine if we put 12 in series.

The detail of the energy/voltage/discharge current is available in the kicad file.

1. Cells position in the module.

If we keep the same size for the global TSAC we need to find a nice way to manage the space.